



**Galgotias College of Engineering & Technology**  
**Department Of Information Technology**  
**Innovation in Teaching Learning**

**Faculty Name: Ms. Kanika Singhal**

**Subject Name: Computer Organization and Architecture(RCS 302)**

**Approach: Question sessions (Viva Voce)**

**List of questions asked during the Session**

1. List the difference between static RAM and dynamic RAM along with its cell.
2. What is disk controller? How it can be used to co figurate auxiliary memory.
3. How a data is organized in the disk? Explain in detail.
4. Define intra segment and inter segment communication between the processors.
5. Mention the group of lines in the system bus. Also explain the types of buses.
6. What are bus master and slave master?
7. Differentiate synchronous and asynchronous bus.
8. What is strobe signal? How it is used in the processor.
9. What is bus arbitration? Explain with types
10. Draw a diagram to implement manual multiplication algorithm.
11. Perform the 2's complement subtraction of smaller number (101011) from larger number (111001).
12. What are the basic operations performed by the processor?
13. Define structural, data, and control hazard conditions in a system.
14. List two conditions when processor can stall.
15. List the types of data hazards.
16. List the techniques used for overcoming hazard.
17. What is instruction level parallelism?
18. What are the types of dependencies? Explain with example.