

Department: Electronics and Communication Engineering

BEC352- DIGITAL SYSTEM DESIGN LAB

LIST OF EXPERIMENT

SUGGESTIVE LIST OF EXPERIMENTS

1. Introduction to digital electronics lab- nomenclature of digital ICs, specifications, study of the data sheet, Concept of Vcc and ground, verification of the truth tables of logic gates using TTL ICs.
2. Implementation of the given Boolean function using logic gates in both SOP and POS forms.
3. Verification of state tables of RS, JK, T and D flip-flops using NAND & NOR gates.
4. Implementation and verification of Decoder using logic gates.
5. Implementation and verification of Encoder using logic gates.
6. Implementation of 4:1 multiplexer using logic gates.
7. Implementation of 1:4 demultiplexer using logic gates.
8. Implementation of 4-bit parallel adder using 7483 IC.
9. Design, and verify the 4-bit synchronous counter.
10. Design, and verify the 4-bit asynchronous counter.
11. Implementation of Mini Project using digital integrated circuits and other components.

Galgotias College of Engineering and Technology
1, Knowledge Park-II, Greater Noida, 201306(Uttar Pradesh)
Department: Electronics and Communication Engineering

BEC352- DIGITAL SYSTEM DESIGN LAB

LIST OF EQUIPMENT

Subject Code of the Lab : BEC352

Name of the Lab : DIGITAL SYSTEM DESIGN LAB

S. No.	Name of Equipment
1	POWER SUPPLY
2	FUNCTION GENERATORS
3	DIGITAL STORAGE OSCILLOSCOPES (DSOs)
4	CATHODE RAY OSCILLOSCOPES (CROs)
5	DIGITAL TRAINER KITS
6	IC's (MUX, DEMUX, ENCODER,DECODER)