Department: Electronics and Communication Engineering

KEC751B –VLSI DESIGNLAB
LIST OF EXPERIMENT

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1. Design and analysis of basic of logic Gates: AND, OR, NOT, NAND, NOR, XOR, XNOR.

2. Design and implementation of Half adder and Full adder using CMOS logic.

3. To simulate the schematic of the common drain amplifier.

4. To simulate the schematic of the differential amplifier.

5. To simulate the schematic of the operational amplifier.

6. Design of 3-8 decoder using MOS technology.

7. Design a 4:1 Multiplexer.

8. Design and implementation of Flip Flop circuit.

9. Layout design of PMOS, NMOS transistors.

10. Layout design of CMOS inverter and its analysis.

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KEC751B –VLSI DESIGN LAB
LIST OF EQUIPMENT/EDA Tools

Subject Code of the Lab: KEC751B

Name of the Lab: VLSI DESIGN LAB

S. No.	Name of Equipment/EDA Tools
1	COMPUTER SYSTEM
2	OrCAD
3	Modelsim 6.1
4	Xilinx ISE