




GALGOTIAS COLLEGE OF ENGINEERING AND TECHNOLOGY
1, Knowledge Park-II, Greater Noida, Uttar Pradesh, 201310

FACULTY PROFILE

Name of Faculty	Dr.S.MOHAMED SULAIMAN			
Designation	ASSOCIATE PROFESSOR			
College and Department	GCET/ECE			
Google Scholar ID	https://scholar.google.com/citations?hl=en&user=q1SfYBkAAAAJ			
Qualification	UG: B.TECH-71%	PG: M.TECH-7.54		
Ph.D (if applicable)	Specialization: VLSI DESIGN Title: Design and Implementation of 16-Bit counter with various logic using process variation technology			
Experience in Years	Teaching: 14.1	Industry:1	Research:0	
No. of Papers Published in Journals	National: 0		International:14	
No. of Paper Published in Conferences	National: 0		International: 03	
No. of Books/ Books Chapters	Books published: 0	Books edited: 0	Book chapters published:0	
No. of Patents	Published: Nil		Granted: Nil	
Number of Ph.D./ PG/ UG Guided	Ph.D: Nil	PG: 04	UG: 28	
Details of Professional Membership	2(IETE,ISTE)			
Details of Achievements/Awards	Nil			
Details of Grants fetched	Nil			
No. of STTPs/ FDPs attended as Resource Person	Nil			
No. of Conferences/ Seminars/ STCs/ STTPs/ FDPs attended	Conferences: 02	Seminar: 03	STCs/ STTPs/ FDPs: 02	
No. of SWAYAM/ MOOCs attended with certification	Nil			