




GALGOTIAS COLLEGE OF ENGINEERING AND TECHNOLOGY
1, Knowledge Park-II, Greater Noida, Uttar Pradesh, 201310

FACULTY PROFILE

Name of Faculty	Dr. Ravendra Singh			
Designation	Associate Professor			
College and Department	GCET, EEE			
Google Scholar ID	https://scholar.google.co.in/citations?user=gteJRVMAAAAJ&hl=en			
Qualification	UG: B. E. (Electronics & Communication)	PG: M. Tech. (Signal Processing)		
Ph.D (if applicable)	Specialization: Analog Signal Processing Title: FTFNTA-based Analog Signal Processing/ Generation Circuits.			
Experience in Years	Teaching: 13	Industry: --	Research: --	
No. of Papers Published in Journals	National: --		International: 12	
No. of Paper Published in Conferences	National: --		International: 07	
No. of Books/ Books Chapters	Books published: --	Books edited: 01	Book chapters published: 03	
No. of Patents	Published: --		Granted: --	
Number of Ph.D./ PG/ UG Guided	Ph.D: --	PG: 08	UG: 12	
Details of Professional Membership	IEEE, IAEGN, IFERP, IRED			
Details of Achievements/Awards	--			
Details of Grants fetched	---			
No. of STTPs/ FDPs attended as Resource Person	--			
No. of Conferences/ Seminars/ STCs/ STTPs/ FDPs attended	Conferences: 04	Seminar: 01	STCs/ STTPs/ FDPs: 06/00/07	
No. of SWAYAM/ MOOCs attended with certification	01			